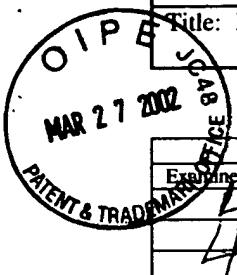


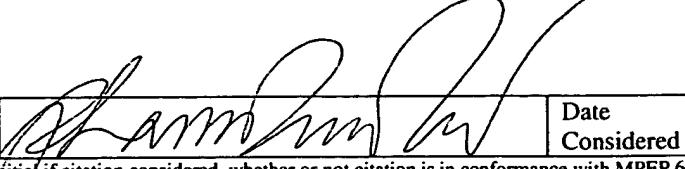
Applicant(s)	Roohparvar et al.	<b>INFORMATION DISCLOSURE STATEMENT FORM PTO-1449</b>
Serial No.	09/943,399	
Filing Date	August 30, 2001	
Group Art Unit	2818	
Confirmation No.	8233	
Attorney Docket No.	400.130US01	
Title: FLASH MEMORY WITH REDRAM INTERFACE		Sheet 1 of 1



U.S. Patent References				
Examiner Initials	Patent No.	Issue Date	Name	Filing Date
LH	5,511,024	04/23/96	Ware et al.	04/23/96
	6,310,814	10/30/01	Hampel et al.	08/08/00
<b>COPY OF PAPERS ORIGINALLY FILED</b>				

Foreign Patent References				
Examiner Initials	Foreign Patent	Name	Publication Date	
	Country	No.		
				APR - 1 2002 TECHNOLOGY CENTER 2800

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
LH	RAMBUS "Direct RDRAM 128/144-Mbit (256Kx16/18x32s)" Document DL0059, Version 1.11 <i>Preliminary Information</i> pp. 1-20 (date not available)

Examiner Signature		Date Considered	12/12/2002
--------------------	--	-----------------	------------

\*Examiner initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Based on Form PTO-FB-A820 Patent and Trademark Office, U.S. Department of Commerce

RECEIVED